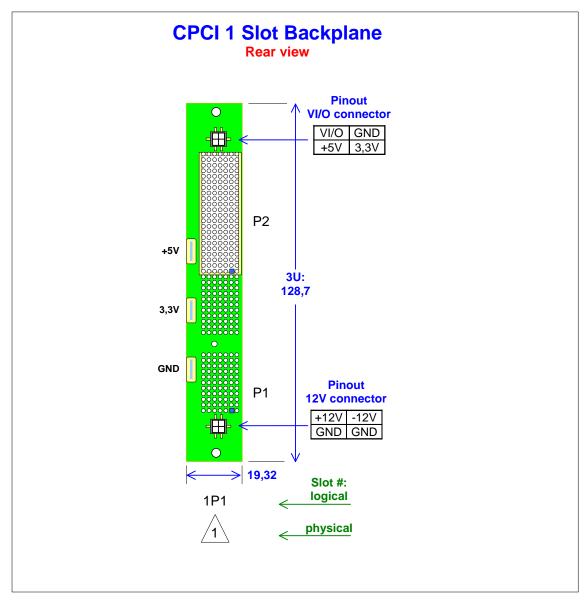


Assembly Instructions & general Information

- 1. **Purpose:** The 1 Slot Backplane provides power to a CPCI CPU Board. There are no bussed signals, connector P2 is compatible to the 32Bit System Slot pinout and comprises Rear I/O functionality.
- 2. **Mechanical Mounting:** Attach the backplane through the mounting holes at top and bottom using M2,5 screws and isolating washers. Creepage and clearance between screw and backplane logic GND are in accordance with EN60950 and is maintained by means of layout when using isolating washers.
- 3. Chassis GND: If noise reduction shall be achieved by connecting logic GND to Chassis GND, use conductive washers instead of isolating ones. Spring washers are recommended instead of flat washers.
- 4. VI/O: VI/O is set by default to +5V. It can be changed to 3,3V by changing the appropriate cable within the VI/O connector (placed on top of P2). Remove the crimp contact from the chamber indicated with +5V and push it into the chamber indicated with 3,3V. Indications are on the drawing #1 on this document, not on the connector. Further change the key from front at connector P1. Blue key: +5V; yellow key: 3,3V. Yellow coding keys can be ordered from Schroff, part# is: 21101-658. Kit contains 8 yellow coding keys and the adequate tool.
- 5. Geographical Addressing (GA) is not supported.



Mechanical and electrical interface



