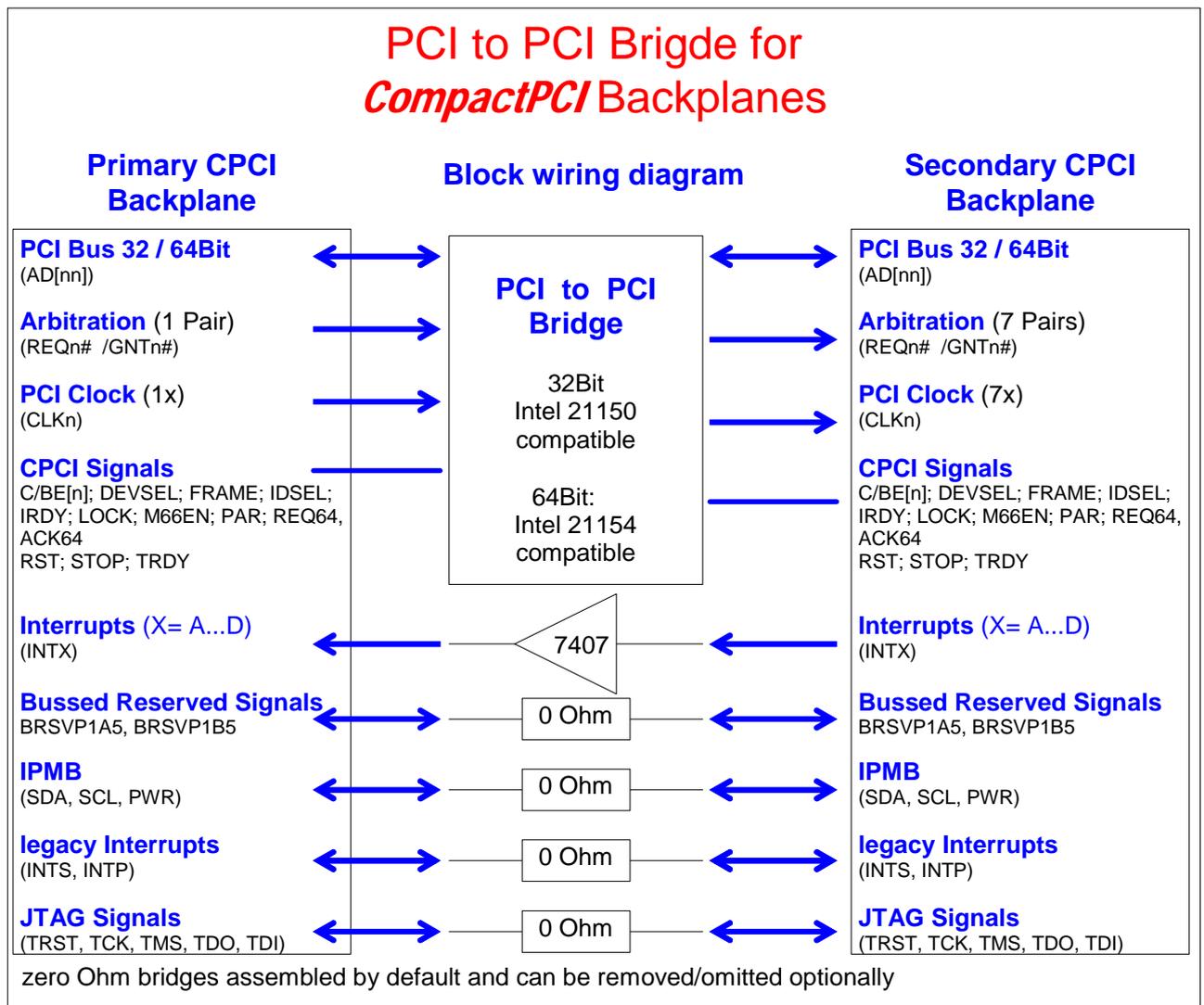
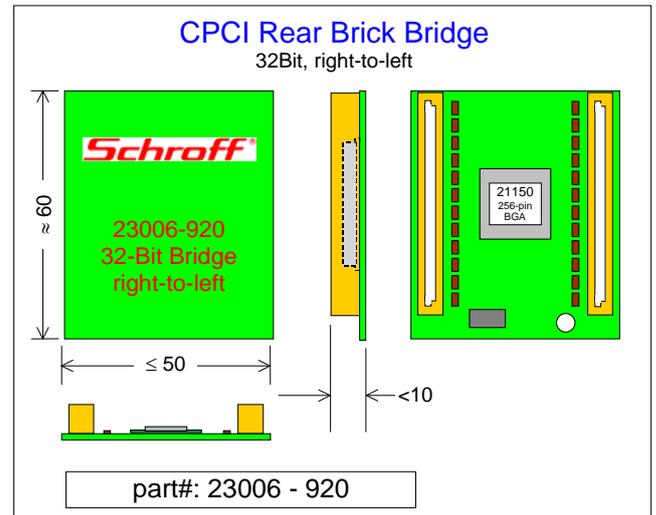
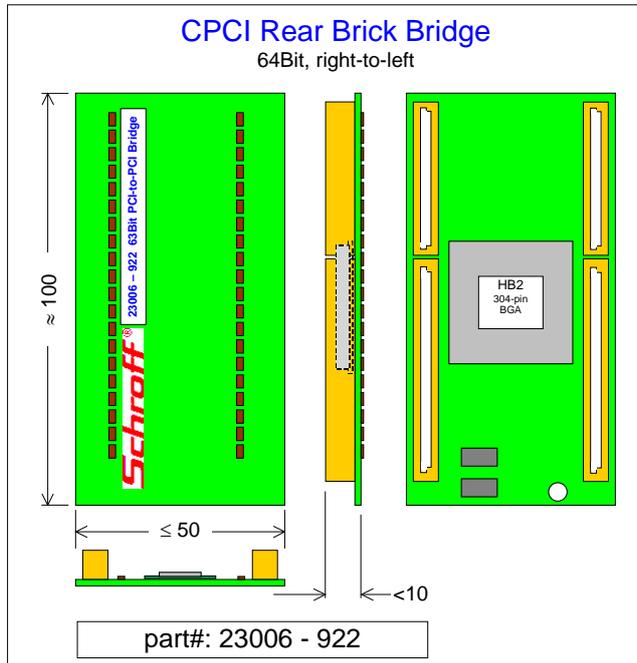


Assembly Instructions & General Information

- Mechanical Mounting:** Please make sure that both backplanes are only attached to the horizontal rails, but not fixed. **The mounting screws of the backplane are not to be tightened until the bridge is plugged and fully seated!**
- Power Supply of Backplanes:** Both backplanes connected by the bridge are to be powered individually. The bridge is not to be used for bridging power. The bridge does not isolate the power rails of both backplanes. GND is connected by a sufficient number of pins in the connector to ensure signal integrity and a common GND potential on both backplanes.
- VI/O:** There is no need to choose the VI/O voltage for the bridge. The bridge automatically takes the VI/O voltage of the primary and secondary side. Both backplanes can be set to different VI/O voltages, e.g. +5V on the primary side and 3,3V on the secondary side.
- M66MHz Operation:** The 64-bit Bridge 23006-922 is capable of 33 and 66 MHz Operation, the 32-bit Bridge 23006-920 is capable of 33MHz Operation.





Mechanical drawing



Schroff CPCI Bridges 32Bit and 64Bit